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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Stephen M. Watts

Serial No.: 10/026,085

Filed: 12/21/2001

For: Parallel Integrated Circuit Test Apparatus and Test Method

Conf. No.: 8279

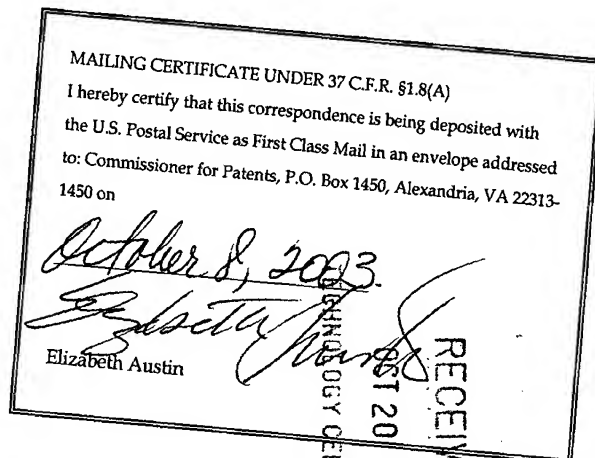
Docket No.: TI-29923

Examiner: Patel, Paresh, H.

Art Unit: 2829

Response under 37 CFR 1.111

Commission for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



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TECHNOLOGY CENTER 2800

Dear Sir:

The following remarks are offered in response to the Examiner's Office Action dated 07/10/03. They are respectfully submitted as a full and complete response to that Action. The Commissioner is hereby requested and authorized to charge any fees necessary for the filing of the enclosed papers to deposit account number 20-0668 of Texas Instruments Incorporated.